



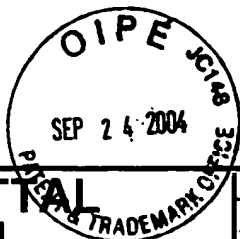
2131
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TRANSMITTAL FORM <i>(to be used for all correspondence after initial filing)</i>	Application No.	09/668,610
	Filing Date	September 22, 2000
	First Named Inventor	Carl M. Ellison
	Art Unit	2131
	Examiner Name	Keveh Abrishamkar
Total Number of Pages in This Submission	Attorney Docket Number	42390P8104X

ENCLOSURES (check all that apply)		
<input checked="" type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment / Response <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input checked="" type="checkbox"/> PTO/SB/08 <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Basic Filing Fee <input type="checkbox"/> Declaration/POA <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s)	<input type="checkbox"/> After Allowance Communication to Group <input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences <input type="checkbox"/> Appeal Communication to Group (Appeal Notice, Brief, Reply Brief) <input type="checkbox"/> Proprietary Information <input type="checkbox"/> Status Letter <input checked="" type="checkbox"/> Other Enclosure(s) (please identify below): <div style="border: 1px solid black; padding: 5px; margin-top: 5px;">-Copies of 6 cited refs. -Cross Reference to Related Applications -Return Postcard</div>
Remarks		

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT	
Firm or Individual name	Gregory D. Caldwell, Reg. No. 39,926 BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP
Signature	
Date	September 21, 2004

CERTIFICATE OF MAILING/TRANSMISSION			
I hereby certify that this correspondence is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.			
Typed or printed name	Kristin A. Morrow		
Signature		Date	September 21, 2004



FEE TRANSMITTAL for FY 2004

Effective 01/01/2004. Patent fees are subject to annual revision.

☐ Applicant claims small entity status. See 37 CFR 1.27.

TOTAL AMOUNT OF PAYMENT (\$) 0.00

Complete if Known

Application Number 09/668,610
Filing Date September 22, 2000
First Named Inventor Carl M. Ellison
Examiner Name Keveh Abrishamkar
Art Unit 2131
Attorney Docket No. 42390P8T04X

METHOD OF PAYMENT (check all that apply)

☐ Check ☐ Credit card ☐ Money Order ☐ Other ☒ None
☐ Deposit Account

Deposit Account Number

02-2666

Deposit Account Name

Blakely, Sokoloff, Taylor & Zafman LLP

The Commissioner is authorized to: (check all that apply)

☐ Charge fee(s) indicated below ☐ Credit any overpayments
☒ Charge any additional fee(s) or underpayment of fees as required under 37 CFR §§ 1.16, 1.17, 1.18 and 1.20.
☐ Charge fee(s) indicated below, except for the filing fee to the above-identified deposit account

FEE CALCULATION

1. BASIC FILING FEE

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
1001	770	2001	385	Utility filing fee	
1002	340	2002	170	Design filing fee	
1003	530	2003	265	Plant filing fee	
1004	770	2004	385	Reissue filing fee	
1005	160	2005	80	Provisional filing fee	
SUBTOTAL (1)					(\$)

2. EXTRA CLAIM FEES

Total Claims 20** = X =
Independent Claims 3 = X =
Multiple Dependent =

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
1202	18	2202	9	Claims in excess of 20	
1201	86	2201	43	Independent claims in excess of 3	
1203	290	2203	145	Multiple Dependent claim, if not paid	
1204	86	2204	43	**Reissue independent claims over original patent	
1205	18	2205	9	**Reissue claims in excess of 20 and over original patent	
SUBTOTAL (2)					(\$)

**or number previously paid, if greater. For Reissues, see below

FEE CALCULATION (continued)

3. ADDITIONAL FEES

Large Entity		Small Entity		Fee Description	Fee Paid
Fee Code	Fee (\$)	Fee Code	Fee (\$)		
1051	130	2051	65	Surcharge - late filing fee or oath	
1052	50	2052	25	Surcharge - late provisional filing fee or cover sheet.	
2053	130	2053	130	Non-English specification	
1812	2,520	1812	2,520	For filing a request for ex parte reexamination	
1804	920*	1804	920*	Requesting publication of SIR prior to Examiner action	
1805	1,840*	1805	1,840*	Requesting publication of SIR after Examiner action	
1251	110	2251	55	Extension for reply within first month	
1252	420	2252	210	Extension for reply within second month	
1253	950	2253	475	Extension for reply within third month	
1254	1,480	2254	740	Extension for reply within fourth month	
1255	2,010	2255	1,005	Extension for reply within fifth month	
1404	330	2401	165	Notice of Appeal	
1402	330	2402	165	Filing a brief in support of an appeal	
1403	290	2403	145	Request for oral hearing	
1451	1,510	2451	1,510	Petition to institute a public use proceeding	
1452	110	2452	55	Petition to revive - unavoidable	
1453	1,330*	2453	665	Petition to revive - unintentional	
1501	1,330	2501	665	Utility issue fee (or reissue)	
1502	480	2502	240	Design issue fee	
1503	640	2503	320	Plant issue fee	
1460	130	2460	130	Petitions to the Commissioner	
1807	50	1807	50	Processing fee under 37 CFR 1.17(q)	
1806	180	1806	180	Submission of Information Disclosure Stmt	
8021	40	8021	40	Recording each patent assignment per property (times number of properties)	
1809	770	1809	385	Filing a submission after final rejection (37 CFR § 1.129(a))	
1810	770	2810	385	For each additional invention to be examined (37 CFR § 1.129(b))	
1801	770	2801	385	Request for Continued Examination (RCE)	
1802	900	1802	900	Request for expedited examination of a design application	
Other fee (specify) _____					No. Fee _____
SUBTOTAL (3)					0.00

* Reduced by Basic Filing Fee Paid

SUBTOTAL (3) (\$) 0.00

SUBMITTED BY

Complete (if applicable)

Name (Print/Type) Gregory D. Caldwell

Registration No. 39,926
(Attorney/Agent)

Telephone (503) 439-8778

Signature

Date 09/21/04

09/668,610

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Carl M. Ellison et al.

Application No.: 09/668,610

Docket No.: P8104X

Filed: 9/22/00

For: *PROTECTING SOFTWARE
ENVIRONMENT IN ISOLATED EXECUTION*

Examiner: Kaveh Abrishamkar

Art Unit: 2131

Phone No.: (703) 305-8892

INFORMATION DISCLOSURE STATEMENT UNDER 37 C.F.R. § 1.97COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Enclosed please find an Information Disclosure Statement, which is being submitted after the mailing of the first Office Action but before the mailing of a Final Office Action. A copy of each cited reference is also enclosed.

In accordance with 37 C.F.R. § 1.97(e), I hereby state that no item of information in the Information Disclosure Statement filed herewith was cited in a communication from a foreign patent office in a counterpart foreign application, and, to my knowledge after making reasonable inquiry, no item of information contained in this Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than 3 months prior to the filing of this Information Disclosure Statement.

The submission of this Information Disclosure Statement is not to be construed as a representation that a search has been made in the subject

09/668,610

application, and is not to be construed as an admission that the information cited in this statement is material to patentability.

Applicants respectfully request that the Examiner consider the cited reference(s), initial each reference on the enclosed Information Disclosure Statement to indicate such consideration, and return a copy of the initialed Information Disclosure Statement to Applicants. If the Examiner has any questions, the Examiner is invited to contact the undersigned at (512) 732-3927.

Respectfully submitted,

Dated: 9/22/04

MBarré
Michael R. Barré
Patent Attorney
Intel Americas, Inc.
Registration No. 44,023
(512) 732-3927

c/o Blakely, Sokoloff, Taylor &
Zafman, LLP
12400 Wilshire Blvd.
Seventh Floor
Los Angeles, CA 90025-1026

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9-21-04

Date of Deposit

Kristin A. Morrow

Name of Person Mailing Correspondence

Kristin A. Morrow

Signature

9-21-04
Date

Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Complete if Known

Application Number	09/668,610
Filing Date	September 22, 2000
First Named Inventor	Carl M. Ellison
Art Unit	2131
Examiner Name	Keveh Abrishamkar
Attorney Docket Number	42390P8104X

Sheet	1	of	1
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U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

Examiner
Signature

Date	Considered
11/1/2011	11/1/2011
11/2/2011	11/2/2011
11/3/2011	11/3/2011
11/4/2011	11/4/2011
11/5/2011	11/5/2011
11/6/2011	11/6/2011
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12/30/2011	12/30/2011
12/31/2011	12/31/2011

*Examiner: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication.

¹Applicant's unique citation designation number (optional). See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. Applicant is to place a check mark here if English language Translation is attached.

Based on PTO/SB/08A (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wlr) 08/11/2003.

Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450

09/668,610



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:

Carl M. Ellison et al.

Application No.: 09/668,610

Docket No.: P8104X

Filed: 9/22/00

For: *PROTECTING SOFTWARE
ENVIRONMENT IN ISOLATED EXECUTION*

Examiner: Kaveh Abrishamkar

Art Unit: 2131

Phone No.: (703) 305-8892

CROSS REFERENCE TO RELATED APPLICATIONS

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Applicants hereby cross-reference the following related patents and patent applications, which are all assigned to the same assignee as the present patent application.

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9-21-04

Date of Deposit

Kristin A. Morrow

Name of Person Mailing Correspondence

Kristin A. Morrow 9-21-04

Signature

Date

09/668,610

Reference No.	Application Serial No.	Patent No.	Title
1	09/672,603		Controlling Accesses To Isolated Memory Using A Memory Controller For Isolated Execution
2	09/822,986		Checking File Integrity Using Signature Generated In Isolated Execution
3	09/538,951		Platform And Method For Issuing And Certifying A Hardware-Protected Attestation Key
4	09/539,344		Managing A Secure Platform Using A Hierarchical Executive Architecture In Isolated Execution Mode
5	09/668,585		Managing A Secure Platform Using A Hierarchical Executive Architecture In Isolated Execution Mode
6	09/541,108		Platform And Method For Remote Attestation Of A Platform
7	09/540,612		Platform And Method For Generating And Utilizing A Protected Audit Log
8	09/540,946		Protecting Software Environment In Isolated Execution
9	09/668,610		Protecting Software Environment In Isolated Execution
10	09/538,954		Generating Isolated Bus Cycles For Isolated Execution
11	09/539,348	6,760,441	Generating A Key Hierarchy For Use In An Isolated Execution Environment
12	09/541,477	6,507,904	Isolated Instructions For Isolated Execution
13	09/540,611		Managing Accesses In A Processor For Isolated Execution
14	09/540,613		Managing A Secure Environment Using A Chipset In Isolated Execution Mode
15	09/668,408		Managing A Secure Environment Using A Chipset In Isolated Execution Mode
16	09/541,667		Attestation Key Memory Device And Bus
17	09/672,602		Attestation Key Memory Device And Bus
18	09/618,738	6,678,825	Controlling Access To Multiple Isolated Memories In An Isolated Execution Environment
19	09/618,489	6,633,963	Controlling Access To Multiple Memory Zones In An Isolated Execution Environment
20	10/683,542		Controlling Access To Multiple Memory Zones In An Isolated Execution Environment
21	09/618,659		Resetting A Processor In An Isolated Execution Environment
22	09/751,586		Resetting A Processor In An Isolated Execution Environment

09/668,610

It may be noted that the above list includes the present application, and the above list partially overlaps the cross-reference to related applications that was filed earlier.

If there are any questions that may be cleared up in a telephone interview, the Examiner is invited to call the undersigned at (512) 732-3927.

Respectfully submitted,

Dated: 9/22/04

M Barré
Michael R. Barré
Patent Attorney
Intel Americas, Inc.
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